

cādence®

MEANINGFUL WORK.
GLOBAL IMPACT.



What makes working at **Cadence** so special? Many things. Cadence plays a critical role in creating the technologies that modern life depends on. We are a **global electronic design automation company**, providing software, hardware and intellectual property to design advanced semiconductor chips that enable our customers create revolutionary products and experiences. Cadence continues to be recognized by Fortune Magazine as **one of the 100 Best Companies to Work For**. With us you have the chance to make an impact and create solutions that will influence the development of new technology. If you speak English and want to do work that matters, then you have the exciting opportunity to join our team.

We are looking for the **Senior Analog IP Design Engineer** to our office in Warsaw.

As our employee you will be responsible for:

- The development and customization of analog IP blocks and subsystems, construct them while providing high-quality consistently and reliably, in a customer driven environment;
- The participation in spec definition and effort estimations, interface with the layout teams on and offsite, produce and present design review documentation according to the internal quality flows and as required to support customer requirements.

If the following requirements are your strengths:

- 5 years of experience in the job offered or a relevant suitable combination of education, training and experience;
- Leading knowledge of the CMOS IC development process – from spec definition, design, post layout verification, manufacture, characterization and production test;
- Experience in at least two of the following areas: High Speed SerDes, ADC and DAC, PLL, LDO, BandGap, Power Management, MIPI and other Interface IPs;
- Track record of delivering successful full-custom analog IP blocks in CMOS technology;
- Relevant experience in 65nm design or in more advanced process nodes;

- A strong understanding of design for mass production and for test;
- Prior exposure to IP packaging, ESD, Signal Integrity, Power Integrity topics;
- Hands-on experience with lab instrumentation & measurement (preferred);
- In depth knowledge of simulation and modelling tools such as Spectre/RF, ADS, Matlab and Simulink;
- Knowledge of Analog IC Layout techniques, matching, signal integrity, parasitics, etc.;
- Ability to work in a fast moving & flexible environment and in multiple projects in parallel.

We are looking for you! As our employee, you can count on:

- Competitive salary package adequate to competencies
- Continuous professional development
- Possibility to cooperate with people from around the world in an expanding global organization
- Private medical care
- Life insurance
- Multisport cards
- Social Fund benefits
- Recommendation bonus
- And much more, so do not hesitate to contact us.

Please send your CV to:

careers_poland@cadence.com

Please provide the following information in your CV: „I hereby agree to the processing of personal data included in my application for the needs necessary to carry out the recruitment process by Cadence Design Systems in accordance with art. 6 par. 1 lit. a Regulation (EU) 2016/679 of the European Parliament and of the Council of 27 April 2016 on the protection of individuals with regard to the processing of personal data and on the free movement of such data and repealing Directive 95/46/EC (General Data Protection Regulation). I also consent to processing of my personal data for the purposes of any future recruitment processes”.